

MEMORIES (monolithic integrated circuit)

Type Number	Case		Package No.	Circuit Functions & Applications	Main Specifications
	Package	Pins & Variation			
LC3518B	DIP	24	3072	CMOS 16K Static RAM (2K×8 Bits), $\overline{CE1}$, $\overline{CE2}$ Pins	Address access time = 120,150ns, low operating current : 9mA (f=1MHz)
LC3518BM	MFP	24	3045B	CMOS 16K Static RAM (2K×8 Bits), $\overline{CE1}$, $\overline{CE2}$ Pins	Miniflat package version of LC3518B
LC3518BS	DIP	24	3114	CMOS 16K Static RAM (2K×8 Bits), $\overline{CE1}$, $\overline{CE2}$ Pins	Slim package version of LC3518B
LC3518BL	DIP	24	3072	CMOS 16K Static RAM (2K×8 Bits), $\overline{CE1}$, $\overline{CE2}$ Pins	Address access time = 120,150ns, low standby current, low operating current : 9mA (f=1MHz)
LC3518BML	MFP	24	3045B	CMOS 16K Static RAM (2K×8 Bits), $\overline{CE1}$, $\overline{CE2}$ Pins	Miniflat package version of LC3518BL
LC3518BSL	DIP	24	3114	CMOS 16K Static RAM (2K×8 Bits), $\overline{CE1}$, $\overline{CE2}$ Pins	Slim package version of LC3518BL
LC3564P	DIP	28	3081	CMOS 64K Static RAM (8K×8 Bits)	Full CMOS, address access time = 100,120ns, standby current = 10 μ A
LC3564PM	MFP	28	3091	CMOS 64K Static RAM (8K×8 Bits)	Full CMOS, address access time = 100,120ns, standby current = 10 μ A
LC3564PL	DIP	28	3081	CMOS 64K Static RAM (8K×8 Bits)	Full CMOS, address access time = 100,120ns, standby current = 1 μ A
LC3564PML	MFP	28	3091	CMOS 64K Static RAM (8K×8 Bits)	Full CMOS, address access time = 100,120ns, standby current = 1 μ A
*LC3564QL	DIP	28	-	CMOS 64K Static RAM (8K×8 Bits)	Full CMOS, address access time = 85,100,120ns, standby current = 1 μ A
*LC3564QML	MFP	28	-	CMOS 64K Static RAM (8K×8 Bits)	Full CMOS, address access time = 85,100,120ns, standby current = 1 μ A
*LC3564QSL	DIP	28	-	CMOS 64K Static RAM (8K×8 Bits)	Full CMOS, address access time = 85,100,120ns, standby current = 1 μ A
⊙LC3664A	DIP	28	3012A	CMOS 64K Static RAM (8K×8 Bits)	Address access time = 80,100,120ns, low operating current : 15mA (f=1MHz)
⊙LC3664ALL	DIP	28	3012A	CMOS 64K Static RAM (8K×8 Bits)	Low standby current (3 μ A at 25°C, 40 μ A at 70°C) Low data retention current (1 μ A at 25°C, 15 μ A at 70°C)
⊙LC3664AL	DIP	28	3012A	CMOS 64K Static RAM (8K×8 Bits)	Address access time = 80,100,120ns, low operating current, low standby current (I _{CCS} = 100 μ A)
⊙LC3664AM	MFP	28	3142A	CMOS 64K Static RAM (8K×8 Bits)	Miniflat package version of LC3664A
⊙LC3664AML	MFP	28	3142A	CMOS 64K Static RAM (8K×8 Bits)	Miniflat package version of LC3664AL
⊙LC3664AMLL	MFP	28	3142A	CMOS 64K Static RAM (8K×8 Bits)	Miniflat package version of LC3664ALL
⊙LC3664AS	DIP	28	3123	CMOS 64K Static RAM (8K×8 Bits)	Slim package version of LC3664A
⊙LC3664ASL	DIP	28	3123	CMOS 64K Static RAM (8K×8 Bits)	Slim package version of LC3664AL
⊙LC3664ASLL	DIP	28	3123	CMOS 64K Static RAM (8K×8 Bits)	Slim package version of LC3664ALL
LC3664N	DIP	28	3012A	CMOS 64K Static RAM (8K×8 Bits)	Address access time = 100,120ns
LC3664NL	DIP	28	3012A	CMOS 64K Static RAM (8K×8 Bits)	Address access time = 100,120ns, low standby current (I _{CCS} = 100 μ A)
LC3664NM	MFP	28	3091	CMOS 64K Static RAM (8K×8 Bits)	Miniflat package version of LC3664N
LC3664NML	MFP	28	3091	CMOS 64K Static RAM (8K×8 Bits)	Miniflat package version of LC3664NL
LC3664R	DIP	28	3081	CMOS 64K Static RAM (8K×8 Bits)	Address access time = 120,150ns, low operating current : 10mA (f=1MHz)
LC3664RL	DIP	28	3081	CMOS 64K Static RAM (8K×8 Bits)	Address access time = 120,150ns, low operating current : 10mA (f=1MHz), low standby current (I _{CCS} = 100 μ A, T _a = 60°C)
LC3664RM	MFP	28	3091	CMOS 64K Static RAM (8K×8 Bits)	Miniflat package version of LC3664R
LC3664RML	MFP	28	3091	CMOS 64K Static RAM (8K×8 Bits)	Miniflat package version of LC3664RL
*LC3664RS	DIP	28	-	CMOS 64K Static RAM (8K×8 Bits)	Slim package version of LC3664R
*LC3664RSL	DIP	28	-	CMOS 64K Static RAM (8K×8 Bits)	Slim package version of LC3664RL
LC3764P	DIP	28	3081	CMOS 64K Mask ROM (8K×8 Bits)	Address access time = 150ns
	MFP	28	3091	CMOS 64K Mask ROM (8K×8 Bits)	Miniflat package version of LC3764P
⊙LC36256P	DIP	28	3081	CMOS 256K Static RAM (32K×8 Bits)	Address access time = 100,120ns, low operating current : 15mA (f=1MHz)
*LC36256PL	DIP	28	3081	CMOS 256K Static RAM (32K×8 Bits)	Address access time = 100,120ns, low operating current : 15mA (f=1MHz) low standby current (I _{CCS} = 100 μ A)
*LC36256 PML	MFP	28	-	CMOS 256K Static RAM (32K×8 Bits)	Miniflat package version of LC36256P
LC37256 P	DIP	28	3081	CMOS 256K Mask ROM (32K×8 Bits)	Address access time = 150ns
	MFP	28	3091	CMOS 256K Mask ROM (32K×8 Bits)	Miniflat package version of LC37256P
LC371000Q	DIP	28	3081	CMOS 1M Mask ROM (128K×8 Bits)	Address access time = 200ns
	MFP	28	3091	CMOS 1M Mask ROM (128K×8 Bits)	Miniflat package version of LC371000Q
*LC371100Q	MFP	32	-	CMOS 1M Mask ROM (128K×8 Bits)	Address access time = 200ns
*LC374000P	DIP	40	3077	CMOS 4M Mask ROM (512K×8 Bits) (256K×16 Bits)	Address access time = 250ns
LM33256K	DIP	16W	3093	NMOS 256K Dynamic RAM (256K×1 Bit)	Access time = 150ns, page mode
LM33256N	DIP	16W	3093	NMOS 256K Dynamic RAM (256K×1 Bit)	Access time = 150ns, page mode
⊙LC321000	DIP	18W	3143	CMOS Dynamic RAM (1M×1 bit)	Address access time = 100,120ns, fast page mode
⊙LC321000J	SOJ	26	3145	CMOS Dynamic RAM (1M×1 bit)	SOJ package version of LC321000
⊙LC321000Z	ZIP	20	3144	CMOS Dynamic RAM (1M×1 bit)	ZIP package version of LC321000
⊙LC324256	DIP	20W	3146	CMOS Dynamic RAM (256K×4 bits)	Address access time = 100,120ns, fast page mode
⊙LC324256J	SOJ	20	3145	CMOS Dynamic RAM (256K×4 bits)	SOJ package version of LC324256
⊙LC324256Z	ZIP	20	3144	CMOS Dynamic RAM (256K×4 bits)	ZIP package version of LC324256
*LE37C01P	DIP	8	3001B	CMOS 1K One-Time PROM (128×8 Bits)	Serial input (DI pin), serial output (DO pin)