

GENERAL

The KW-524H is an I/O card which provides your 80286/80386/80486/Pentium® based personal computer with Serial/Parallel/Game control interface.

SPECIFICATION

- * Two RS-232C serial ports.
COM1, COM2, COM3 or COM4 selectable,
IRQ3, IRQ4, IRQ5 or IRQ9 selectable.
- * One Bi-directional parallel port.
LPT2 or LPT3 selectable.
- * One game port.
- * Serial/Parallel ports can be disabled.
- * KW-524H supports standard serial/parallel interface.
- * KW-524H-1 (V1.0) supports high speed serial (16 Bytes FIFO), and standard parallel interface.
- * KW-524H-2 (V2.0) supports high speed serial (16 Bytes FIFO), and high speed parallel(83 Bytes FIFO) interface.

BASE ADDRESS

COM1: 3F8-3FF Hex

LPT2: 378 Hex

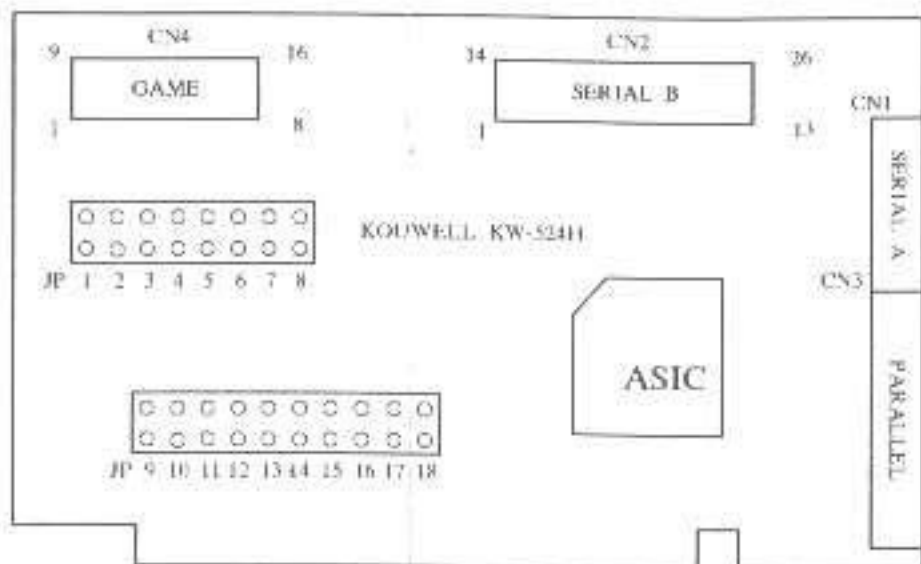
COM2: 2F8-2FF Hex

LPT3: 278 Hex

COM3: 3E8-3EF Hex

COM4: 2E8-2EF Hex

BOARD LAYOUT



CONNECTORS

CN1 : Serial Port A

CN3 : Parallel Port

CN2 : Serial Port B

CN4 : Game Port

When connecting the signal cables, please align color stripped edge of the cable with the Pin-1 of each connector on the board.

JUMPER SETTINGS

<u>Serial Port A</u>	<u>Address and IRQ Level</u>
JP1 Short	COM1 *
JP2 Short	COM3
JP3 Short	COM2
JP4 Short	COM4
JP1 — JP4 all Opened	Disabled
JP11 short	IRQ9
JP12 short	IRQ3
JP13 short	IRQ4 *
JP14 short	IRQ5

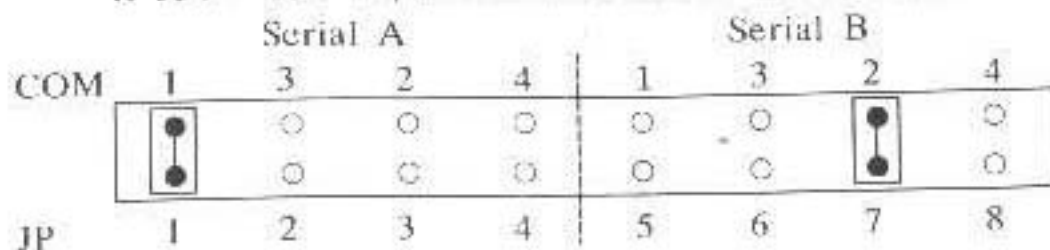
Serial Port B	Address and IRQ Level
JP5 Short	COM1
JP6 Short	COM3
JP7 Short	COM2 *
JP8 Short	COM4
JP5 – JP8 all Opened	Disabled
JP15 Short	IRQ9
JP16 Short	IRQ3 *
JP17 Short	IRQ4
JP18 Short	IRQ5

Parallel Port	Address and IRQ Level
JP9 Short	LPT2 *
JP10 Short	LPT3
JP9 and JP10 Opened	Disabled

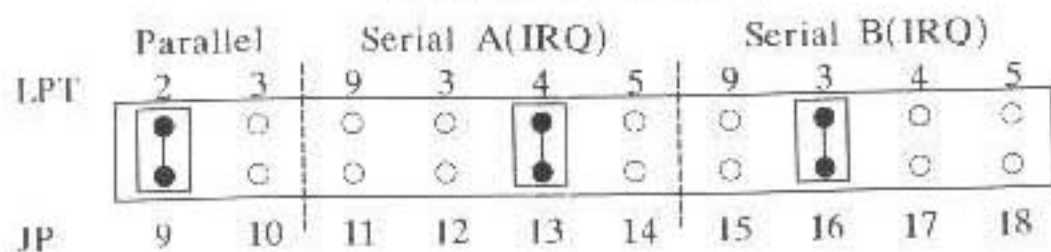
* : Factory Settings

DIAGRAM OF FACTORY SETTINGS

1. JP1 – JP8 : I/O Base Address of Serial Ports



2. JP9 – JP18 : I/O Base Address of Parallel and IRQ Level of Serial Ports



Note : Rectangle represents short-circuiting