

CONNECTOR	PIN-OUTS	SIGNAL NAME
J4, J13 HDD_LED Connector	1 2	LED + LED -
	2, 19, 22 24, 26, 30 40	Reset hard disk Ground
	3	HDD7
	4	HDD8
	5	HDD6
	6	HDD9
	7	HDD5
	8	HDD10
	9	HDD4
	10	HDD11
	11	HDD3
	12	HDD12
	13	HDD2
	14	HDD13
	15	HDD1
	16	HDD14
	17	HDD0
	18	HDD15
	20, 21, 29	NC
	23	HDD10 write
	25	HDD10 read
	27	IOCHRDY
	28	HDD address latch
	31	IRQ14
	32	IOCS#6
	33	HDD11
	34	HDD12
	35	HDD4
	36	HDD13
	37	HDD1
	38	HDD14
	39	HDD15
		HDD active

Table 2-14. Local IDE Connector Pin Definitions

CONNECTOR	PIN-OUTS	SIGNAL NAME
CN10 Secondary IDE Connector	31	IRQ 15 (The rest are same as J6, page 2-10)
J13 HDD_LED Connector	1 2	LED + LED -

Table 2-15. ISA IDE Connector Pin Definitions

JUMPER	PIN DEFINITION
J6	short IDE connector pin27 fixed to IOCHRDY signal open IDE connector pin27 open (default)
J11	short IDE connector pin28 fixed to BALE signal open IDE connector pin28 open (default)

Table 2-16. ISA IDE Jumper Definitions

VESA Bus Connector

The cache system board provides two high-performance VESA bus connectors, SL7 and SL8, for use with VESA peripherals. The VESA bus connector can be utilized for one Local Bus Master and one Local Bus Slave either (SL7) or (SL8). The following tables give the pin assignments for SL7 and SL8. Side A of the connector are pin outs on the board's component side while Side B are pin outs on the board's solder side. Jumpers JVI1 and JVI2 give more information on settings on the motherboard and the VL-bus controller.

JUMPER	PIN DEFINITION
JVI1	High speed write select 1-2 One wait write 2-3 Zero wait write (default)
JVI2	CPU speed select 1-2 Greater than 33MHz 2-3 Less than or equal to 33MHz (default)

Table 2-17. VL-Bus Controller Jumper Settings